

BFX_UA300 Main Board DVT3 change list

- Page 6: Change GPIO [32] net name from "PD_AD82584F" to "CTRL_A113X" for A113X standby control of new AMP board.

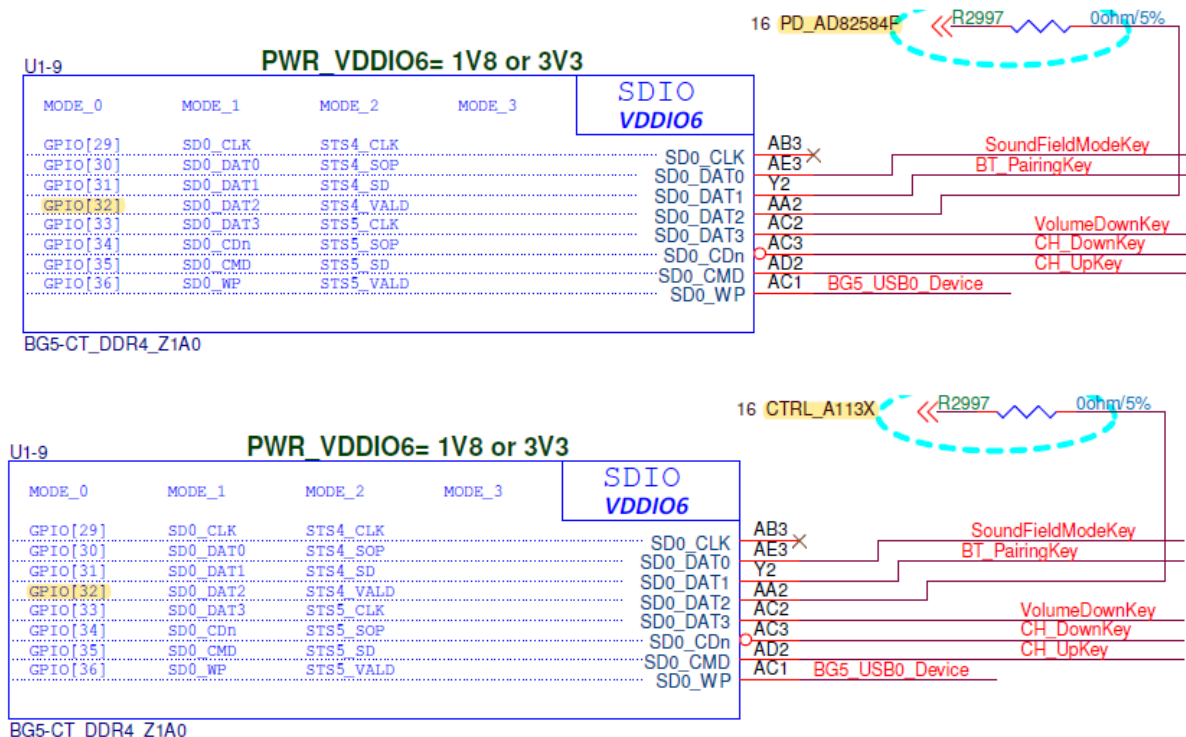


Figure1. GPIO [32] net name, upper: DVT2, lower: DVT3

- Page 6: Change GPIO [19] net name from "PD_AD83586B" to "AMP_PWM_OFF" for AMP IC power down control of new AMP board.

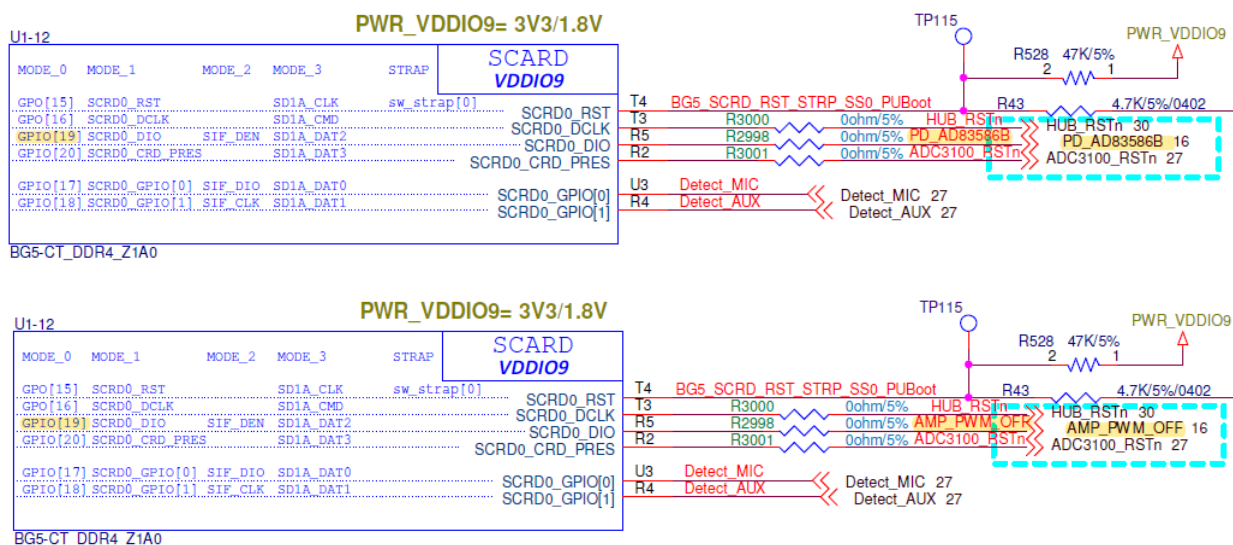


Figure2. GPIO [19] net name, upper: DVT2, lower: DVT3

3. Page 7: New add GPIO [24] as net "IO24_USB_SW_S" for USB signal switch control.
4. Page 7: New add GPIO [27] as net "IO27_AMP_Reset" for reset AMP IC of new AMP board.

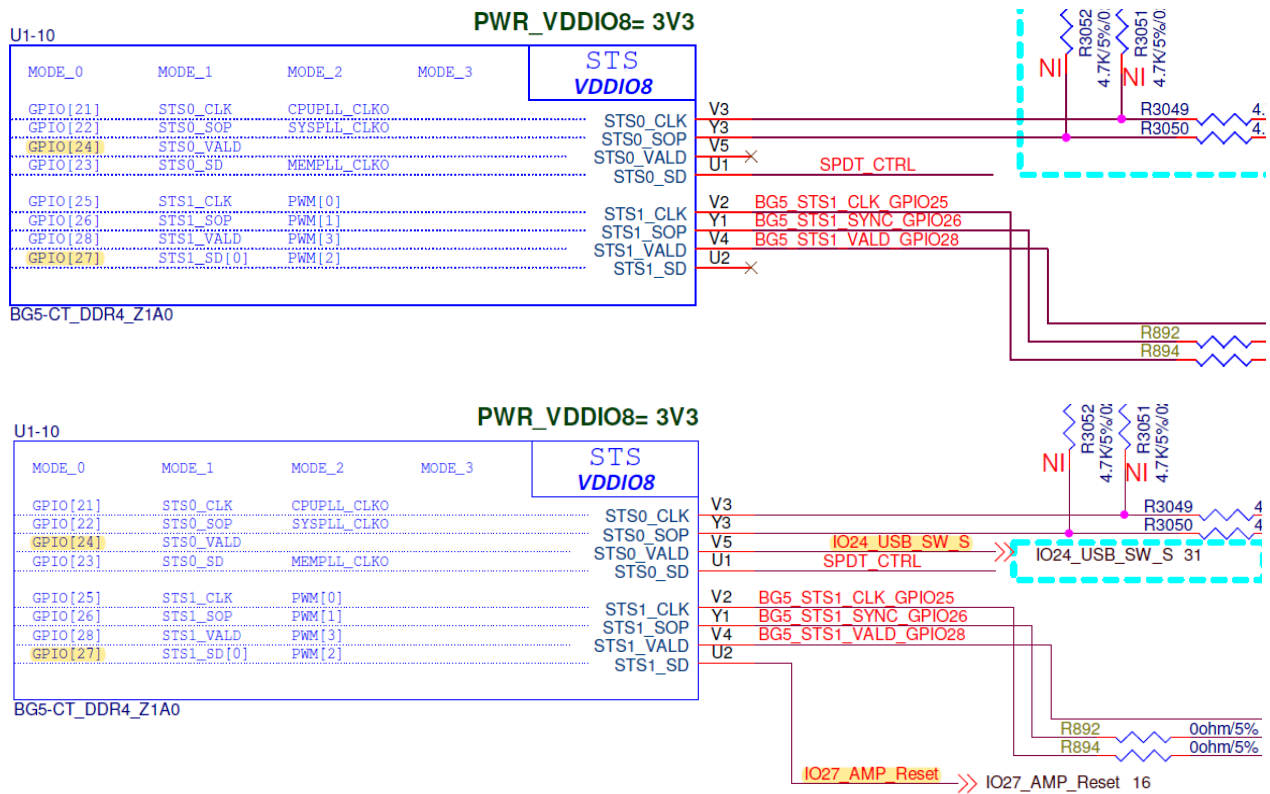


Figure3. GPIO [24] and GPIO [27] net name, upper: DVT2, lower: DVT3

5. Page 7: Add buffer IC (U1321) at I2S bus for better I2S waveform quality of MIC board.

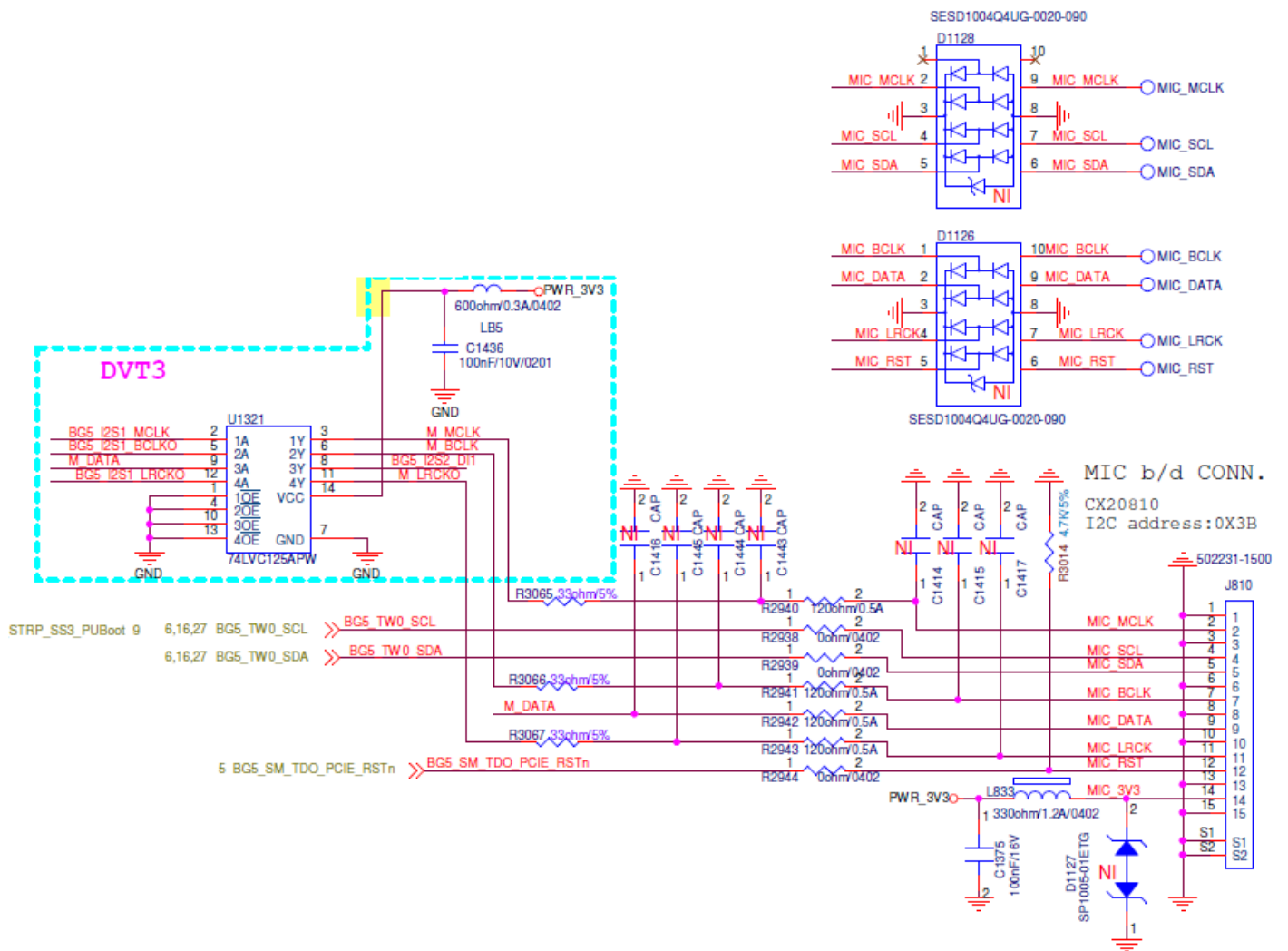


Figure4. Buffer IC U1321 and J810 (MIC board connector) circuit

6. Page 16: Add buffer IC (U1320) at I2S bus for better I2S waveform quality of AMP board and change pin definition of pin 1/2/3/4/5/6 of J814 (pin1: A113X_USB_DM, pin 2: A113X_USB_DP, pin 3: GND, pin 4: AMP_PWM_OFF, pin 5: IO27_AMP_Reset, pin 6: CTRL_A113X).
7. Page 16: Remove 3.3V power circuit (R2981, C1384, C1385) at pin2/3 of J814.

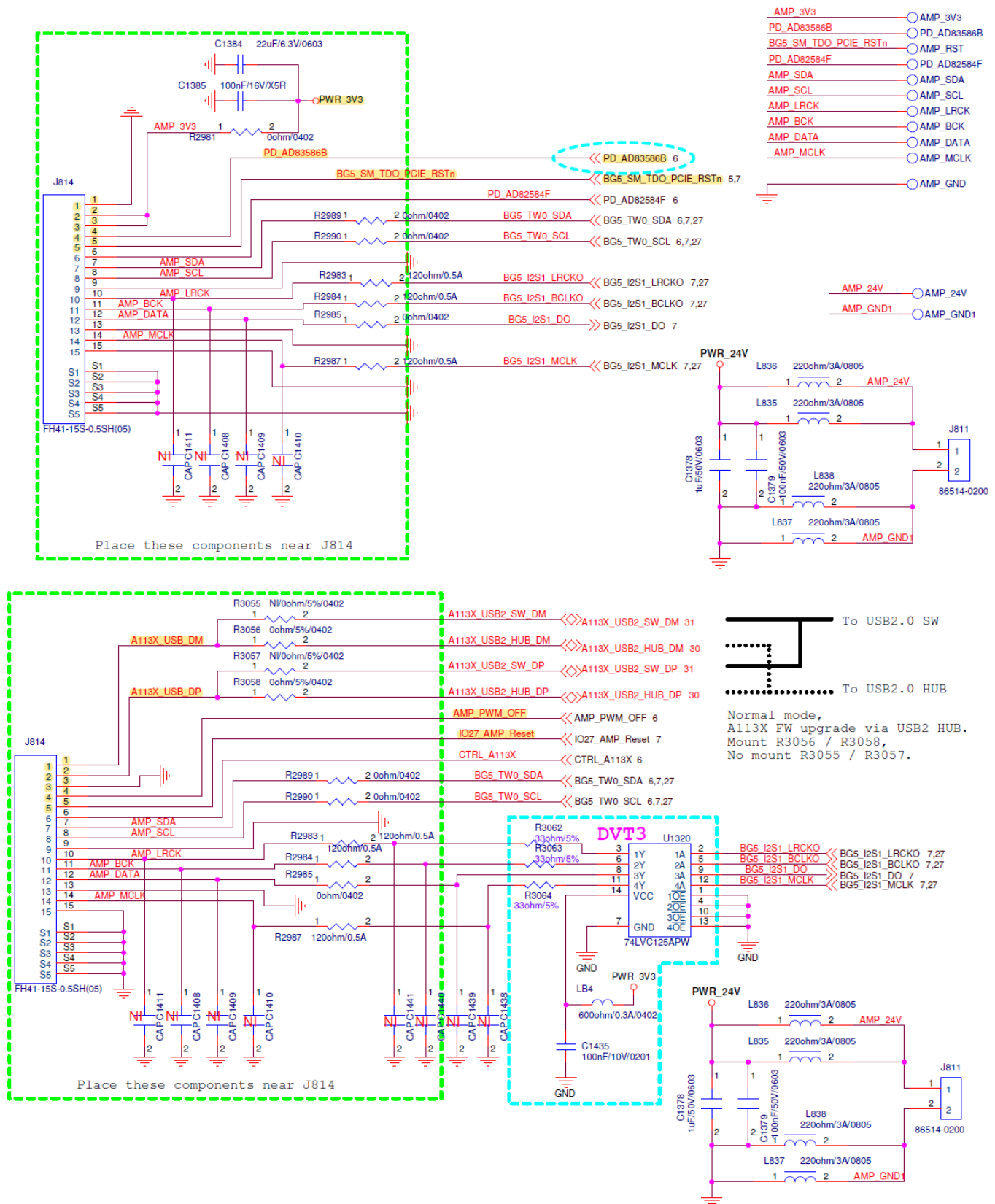


Figure5. J814 (AMP board connector) circuit, upper: DVT2, lower: DVT3

8. Page 23: Change USB2.0 CONN. D-/D+ connection from SOC VS-550 to USB signal SW circuit.

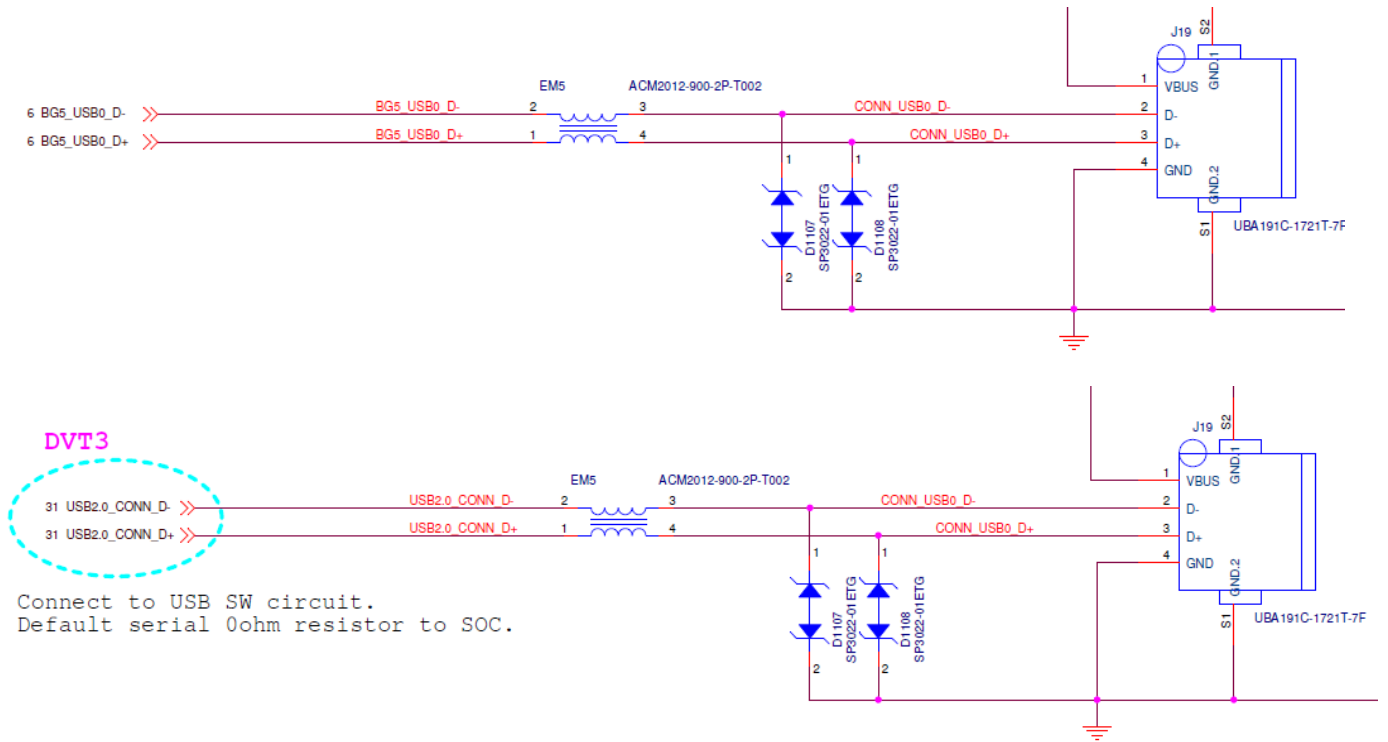


Figure6. USB2.0 connector circuit, upper: DVT2, lower: DVT3

9. Page 27: Add buffer IC (U1319) at I2S bus for better I2S waveform quality of ADC3100.

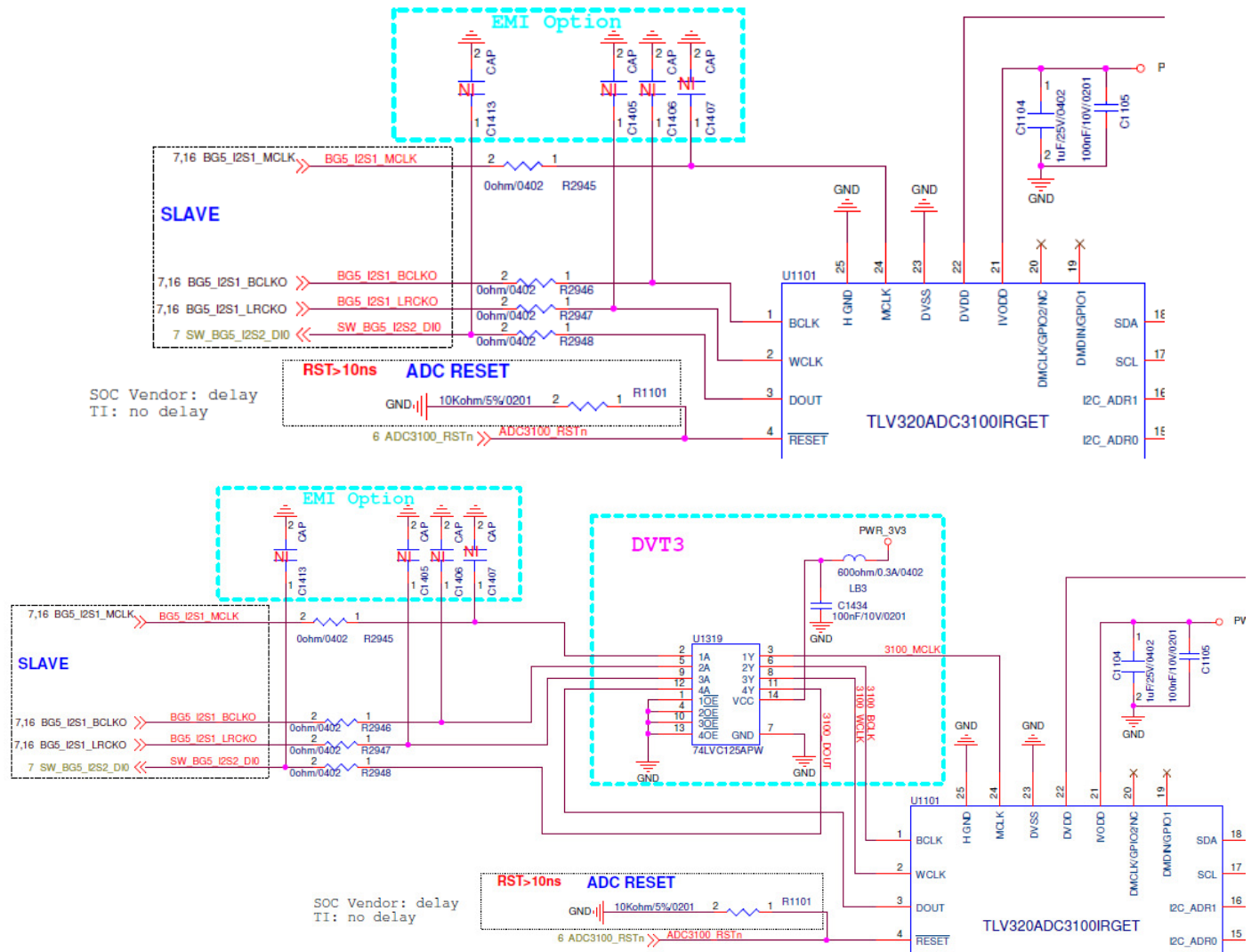


Figure7. Buffer IC U1319 and U1101 (TLV320ADC3100) circuit

10. Page 30: Add nets at pin DM3/DP3 of USB HUB for A113X software download path.

11. Page 30: Remove R3009 to enable port3 of USB HUB.

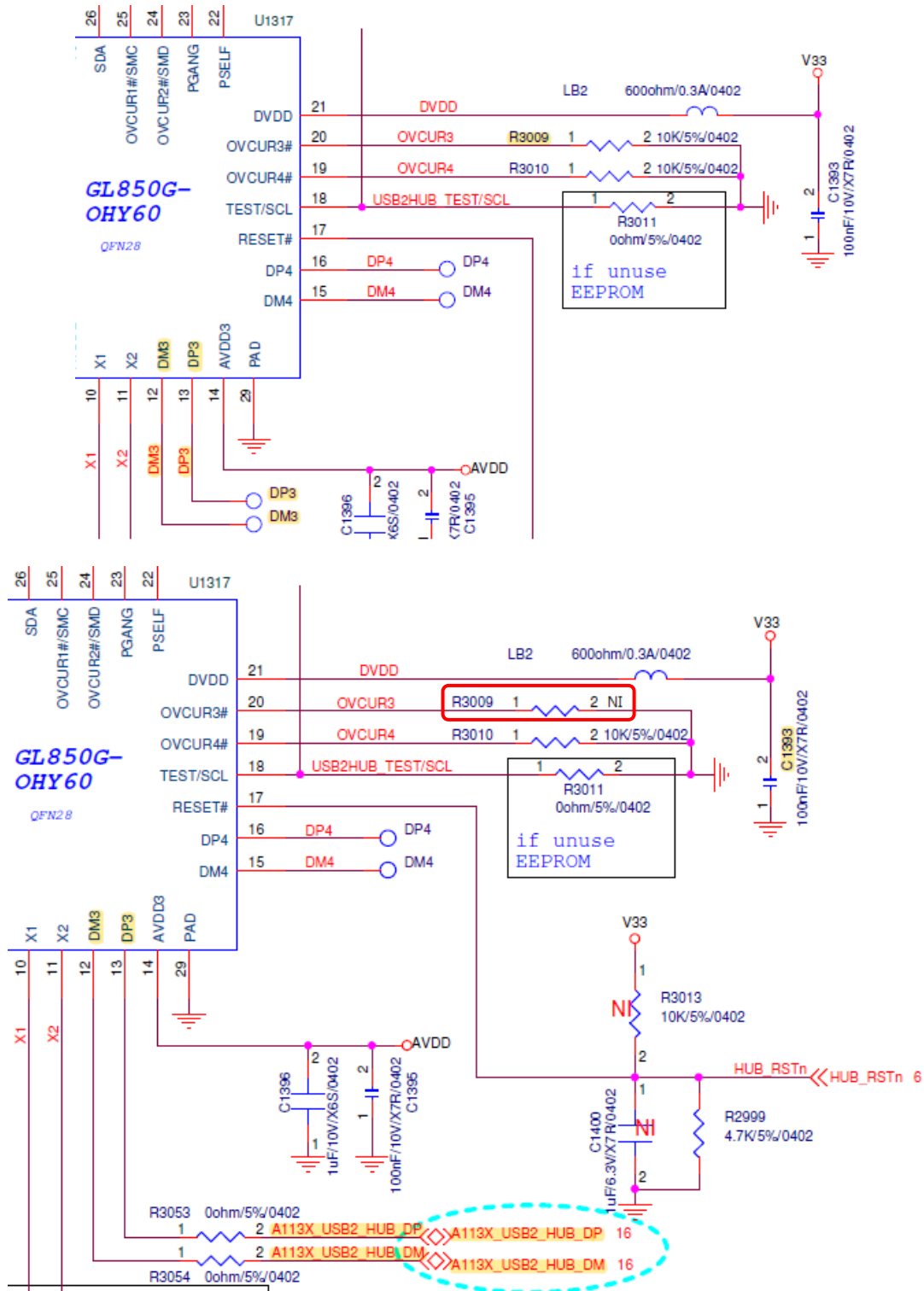
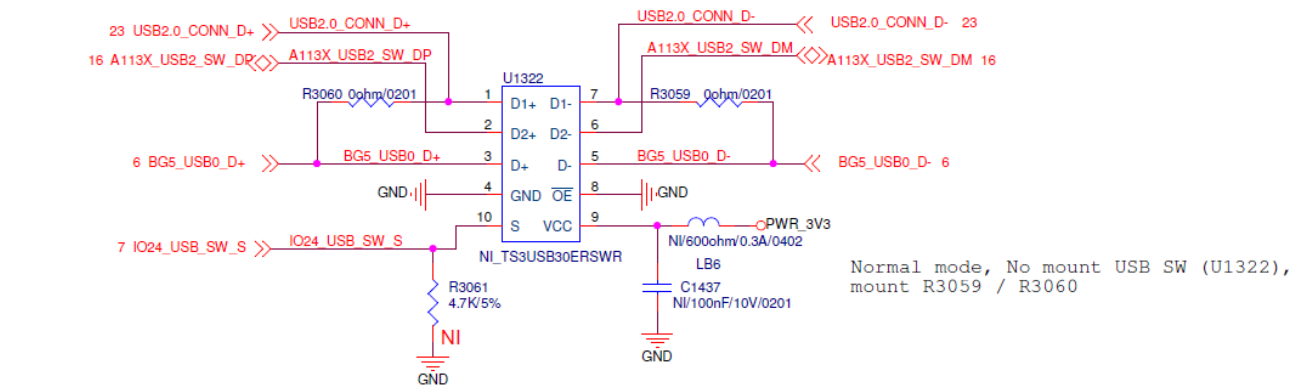


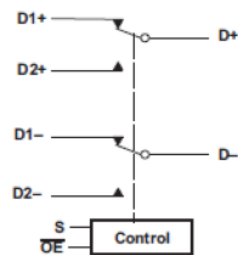
Figure8. Add port3 connection and NI R3009

12. Page 31: Option USB signal SW circuit for A113X software download path. Default USB0 of VS-550 connects directly to USB 2.0 connector via 0201/0ohm resistors.



TRUTH TABLE		
S	OE	FUNCTION
X	H	Disconnect
L	L	D = D1
H	L	D = D2

BLOCK DIAGRAM



Option3(Reserved option1 & 2)

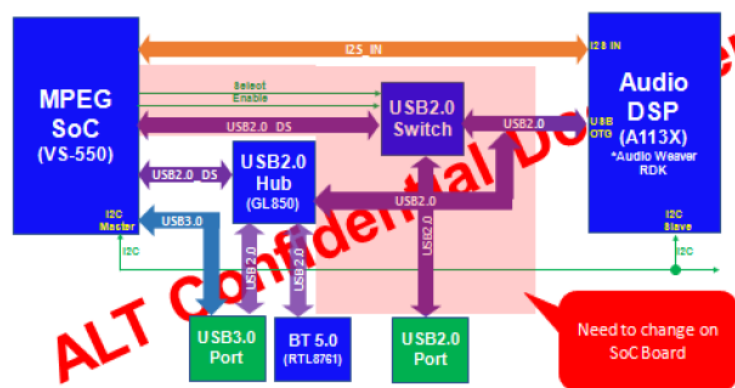


Figure9. Option USB2.0 signal switch

Note 1: If ALT use new AMP board with DVT1/2 main board, PLS rework item 6/7/11. I attached a rework guide named "New AMP +DVT2 MB rework SOP.pdf" in mantis 0001143